1/7

FIG 1

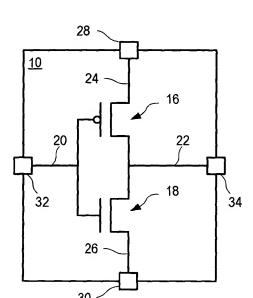
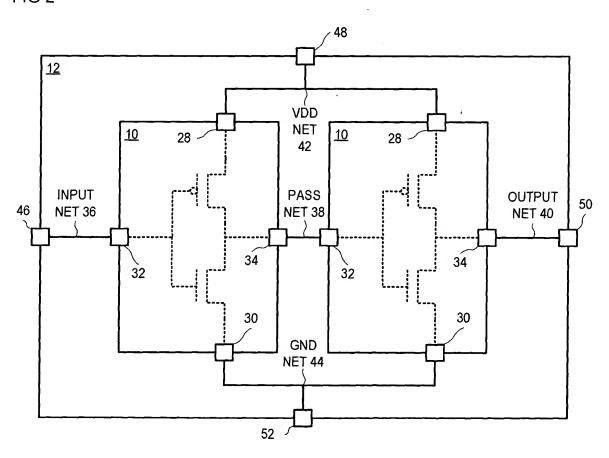
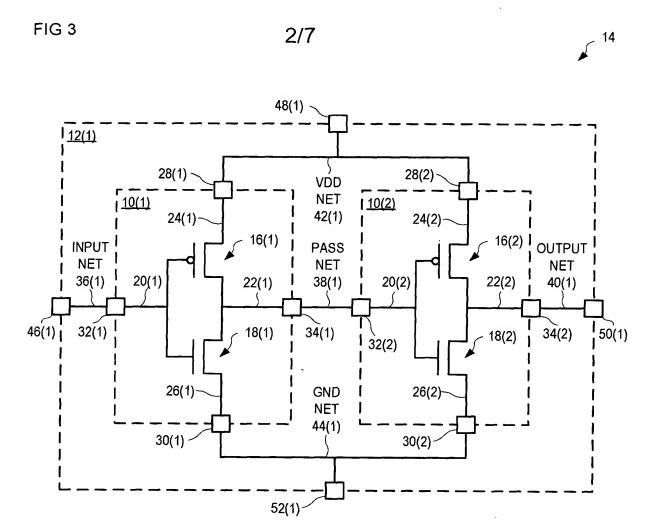


FIG 2





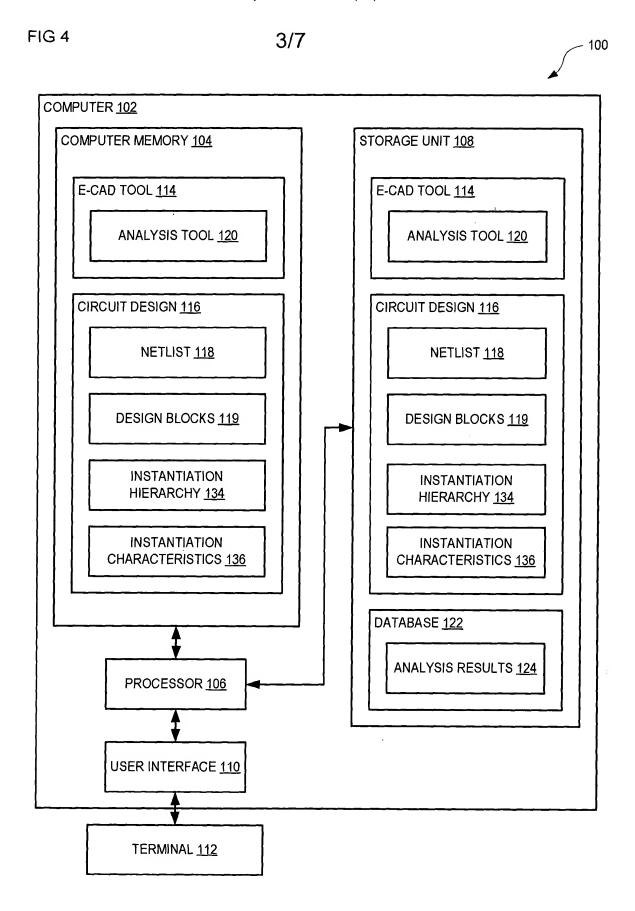
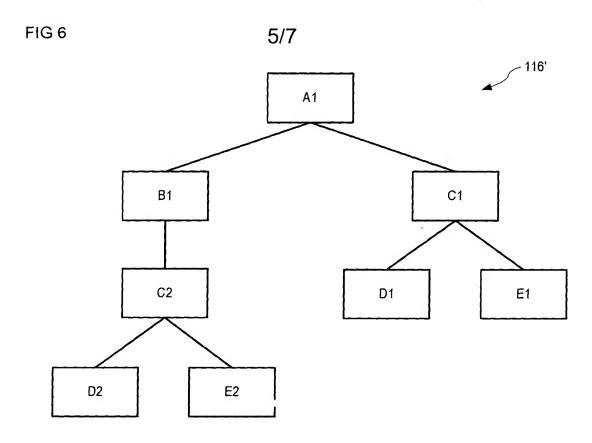


FIG 5 4/7 В С С C D Ε D

Ε

116'

Title: Systems And Methods For Performing Circuit Analysis On A Circuit Design Inventors: S. Brandon Keller, Gregory Dennis Rogers, & George Harold Robbert Docket: 100111260-1 -- Attorney: William P. O'Meara (970) 898-7917



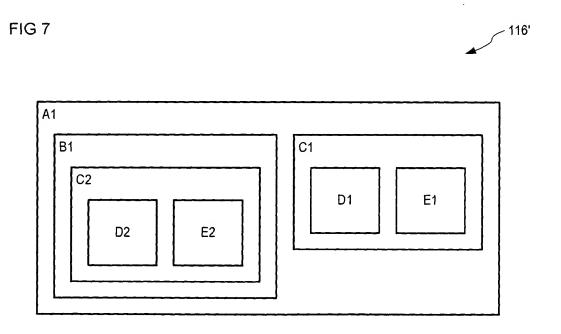
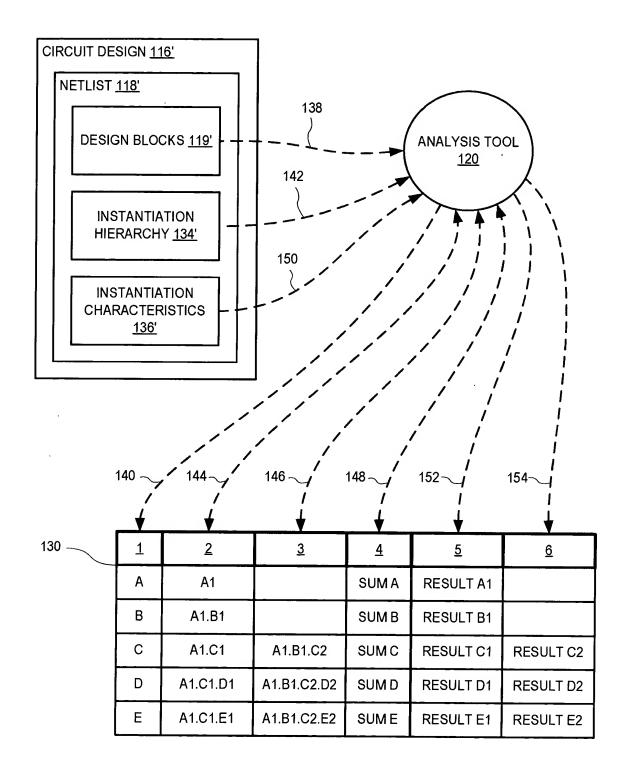


FIG 8

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